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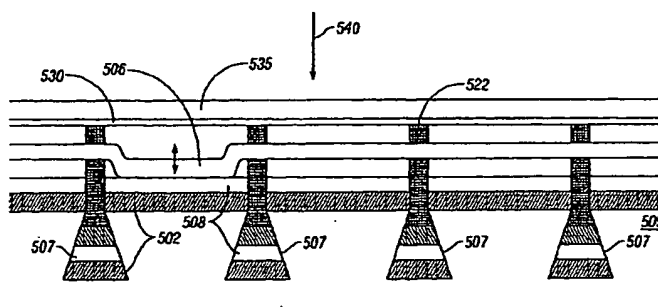
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(54) Title: MEMS DEVICE FABRICATED ON A PRE-PATTERNED SUBSTRATE



(57) Abstract: A microelectromechanical systems device fabricated on a pre-patterned substrate having grooves formed therein. A lower electrode is deposited over the substrate and separated by an orthogonal upper electrode by a cavity. The upper electrode is configured to be movable to modulate reflected light. A semi-reflective layer and a transparent material are formed over the movable upper electrode.

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MEMS DEVICE FABRICATED ON A PRE-PATTERNED SUBSTRATE

Background

Field

[0001] The field of the invention relates to microelectromechanical systems (MEMS) and the packaging of such systems. More specifically, the field of the invention relates to interferometric modulators and methods of fabricating such interferometric modulators on a pre-patterned substrate.

Description of the Related Technology

[0002] Microelectromechanical systems (MEMS) include micro mechanical elements, actuators, and electronics. Micromechanical elements may be created using deposition, etching, and or other micromachining processes that etch away parts of substrates and/or deposited material layers or that add layers to form electrical and electromechanical devices. One type of MEMS device is called an interferometric modulator. As used herein, the term interferometric modulator or interferometric light modulator refers to a device that selectively absorbs and/or reflects light using the principles of optical interference. In certain embodiments, an interferometric modulator may comprise a pair of conductive plates, one or both of which may be transparent and/or reflective in whole or part and capable of relative motion upon application of an appropriate electrical signal. In a particular embodiment, one plate may comprise a stationary layer deposited on a substrate and the other plate may comprise a metallic membrane separated from the stationary layer by an air gap or cavity. As described herein in more detail, the position of one plate in relation to another can change the optical interference of light incident on the interferometric modulator. Such devices have a wide range of applications, and it would be beneficial in the art to utilize and/or modify the characteristics of these types of devices so that their features can be exploited in improving existing products and creating new products that have not yet been developed.

Summary of Certain Embodiments

[0003] The system, method, and devices of the invention each have several aspects, no single one of which is solely responsible for its desirable attributes. Without limiting the scope of this invention, its more prominent features will now be discussed briefly. After considering this discussion, and particularly after reading the section entitled "Detailed Description of Certain Embodiments" one will understand how the features of this invention provide advantages over other display devices. The embodiments described herein provide a package structure and a method of manufacturing a package structure in ambient conditions.

[0004] An embodiment provides a method of making a microelectromechanical systems device. A substrate having a plurality of trenches is provided. At least one layer is deposited over the substrate, wherein the layer is discontinuous at the trenches. A first cavity is created between a first electrode formed over the substrate and a second electrode, wherein the at least one layer comprises the first electrode.

[0005] According to another embodiment, a display device is provided, comprising a substrate having a plurality of grooves formed therein, a first electrode formed over a top surface of the substrate and a second electrode, a semi-reflective layer, and a transparent material formed over the chromium layer. The first electrode and the second electrode are insulated from each other and separated by a first cavity. The semi-reflective layer separated from the second electrode by a second cavity.

[0006] According to yet another embodiment, a method of forming a microelectromechanical systems device is provided. A substrate having a top surface is provided, wherein a plurality of grooves is formed in the top surface. At least one layer is deposited over the substrate, wherein the at least one layer comprises a first conductive material and is discontinuous at the grooves forming rows of the layer on the top surface. A second conductive material is deposited, wherein the second conductive material is oriented orthogonally to the first conductive material on the top surface.

[0007] In accordance with another embodiment, a display device is provided. The display device comprises a substrate having a plurality of grooves formed therein, a first

reflecting means for reflecting light formed over a top surface of the substrate and a second reflecting means for reflecting light, a semi-reflective layer separated from the second reflecting means by a second cavity, and a viewing means for transmitting light. The first reflecting means and the second reflecting means are insulated from each other and separated by a first cavity, and the viewing means formed over the semi-reflective layer.

Brief Description of the Drawings

[0008] These and other aspects of the invention will be readily apparent from the following description and from the appended drawings (not to scale), which are meant to illustrate and not to limit the invention, and wherein:

[0009] FIG. 1 is an isometric view depicting a portion of one embodiment of an interferometric modulator display in which a movable reflective layer of a first interferometric modulator is in a relaxed position and a movable reflective layer of a second interferometric modulator is in an actuated position.

[0010] FIG. 2 is a system block diagram illustrating one embodiment of an electronic device incorporating a 3x3 interferometric modulator display.

[0011] FIG. 3 is a diagram of movable mirror position versus applied voltage for one exemplary embodiment of an interferometric modulator of FIG. 1.

[0012] FIG. 4 is an illustration of a set of row and column voltages that may be used to drive an interferometric modulator display.

[0013] FIG. 5A illustrates one exemplary frame of display data in the 3x3 interferometric modulator display of FIG. 2.

[0014] FIG. 5B illustrates one exemplary timing diagram for row and column signals that may be used to write the frame of FIG. 5A.

[0015] FIGS. 6A and 6B are system block diagrams illustrating an embodiment of a visual display device comprising a plurality of interferometric modulators.

[0016] FIG. 7A is a cross section of the device of FIG. 1.

[0017] FIG. 7B is a cross section of an alternative embodiment of an interferometric modulator.

[0018] FIG. 7C is a cross section of another alternative embodiment of an interferometric modulator.

[0019] FIG. 7D is a cross section of yet another alternative embodiment of an interferometric modulator.

[0020] FIG. 7E is a cross section of an additional alternative embodiment of an interferometric modulator.

[0021] FIGS. 8A-8C are cross-sections of an interferometric modulator formed on a pre-patterned substrate, in accordance with an embodiment.

[0022] FIG. 8D is a cross-section of an interferometric modulator formed on a pre-patterned substrate, in accordance with another embodiment.

[0023] FIG. 8E is a cross-section of an interferometric modulator formed on a pre-patterned substrate, in accordance with yet another embodiment.

Detailed Description of Certain Embodiments

[0024] The following detailed description is directed to certain specific embodiments of the invention. However, the invention can be embodied in a multitude of different ways. In this description, reference is made to the drawings wherein like parts are designated with like numerals throughout. As will be apparent from the following description, the embodiments may be implemented in any device that is configured to display an image, whether in motion (e.g., video) or stationary (e.g., still image), and whether textual or pictorial. More particularly, it is contemplated that the embodiments may be implemented in or associated with a variety of electronic devices such as, but not limited to, mobile telephones, wireless devices, personal data assistants (PDAs), hand-held or portable computers, GPS receivers/navigators, cameras, MP3 players, camcorders, game consoles, wrist watches, clocks, calculators, television monitors, flat panel displays, computer monitors, auto displays (e.g., odometer display, etc.), cockpit controls and/or displays, display of camera views (e.g., display of a rear view camera in a vehicle), electronic photographs, electronic billboards or signs, projectors, architectural structures, packaging, and aesthetic

structures (e.g., display of images on a piece of jewelry). MEMS devices of similar structure to those described herein can also be used in non-display applications such as in electronic switching devices.

[0025] One interferometric modulator display embodiment comprising an interferometric MEMS display element is illustrated in Figure 1. In these devices, the pixels are in either a bright or dark state. In the bright ("on" or "open") state, the display element reflects a large portion of incident visible light to a user. When in the dark ("off" or "closed") state, the display element reflects little incident visible light to the user. Depending on the embodiment, the light reflectance properties of the "on" and "off" states may be reversed. MEMS pixels can be configured to reflect predominantly at selected colors, allowing for a color display in addition to black and white.

[0026] Figure 1 is an isometric view depicting two adjacent pixels in a series of pixels of a visual display, wherein each pixel comprises a MEMS interferometric modulator. In some embodiments, an interferometric modulator display comprises a row/column array of these interferometric modulators. Each interferometric modulator includes a pair of reflective layers positioned at a variable and controllable distance from each other to form a resonant optical cavity with at least one variable dimension. In one embodiment, one of the reflective layers may be moved between two positions. In the first position, referred to herein as the relaxed position, the movable reflective layer is positioned at a relatively large distance from a fixed partially reflective layer. In the second position, referred to herein as the actuated position, the movable reflective layer is positioned more closely adjacent to the partially reflective layer. Incident light that reflects from the two layers interferes constructively or destructively depending on the position of the movable reflective layer, producing either an overall reflective or non-reflective state for each pixel.

[0027] The depicted portion of the pixel array in Figure 1 includes two adjacent interferometric modulators 12a and 12b. In the interferometric modulator 12a on the left, a movable reflective layer 14a is illustrated in a relaxed position at a predetermined distance from an optical stack 16a, which includes a partially reflective layer. In the interferometric modulator 12b on the right, the movable reflective layer 14b is illustrated in an actuated position adjacent to the optical stack 16b.

[0028] The optical stacks 16a and 16b (collectively referred to as optical stack 16), as referenced herein, typically comprise of several fused layers, which can include an electrode layer, such as indium tin oxide (ITO), a partially reflective layer, such as chromium, and a transparent dielectric. The optical stack 16 is thus electrically conductive, partially transparent and partially reflective, and may be fabricated, for example, by depositing one or more of the above layers onto a transparent substrate 20. In some embodiments, the layers are patterned into parallel strips, and may form row electrodes in a display device as described further below. The movable reflective layers 14a, 14b may be formed as a series of parallel strips of a deposited metal layer or layers (orthogonal to the row electrodes of 16a, 16b) deposited on top of posts 18 and an intervening sacrificial material deposited between the posts 18. When the sacrificial material is etched away, the movable reflective layers 14a, 14b are separated from the optical stacks 16a, 16b by a defined gap 19. A highly conductive and reflective material such as aluminum may be used for the reflective layers 14, and these strips may form column electrodes in a display device.

[0029] With no applied voltage, the cavity 19 remains between the movable reflective layer 14a and optical stack 16a, with the movable reflective layer 14a in a mechanically relaxed state, as illustrated by the pixel 12a in Figure 1. However, when a potential difference is applied to a selected row and column, the capacitor formed at the intersection of the row and column electrodes at the corresponding pixel becomes charged, and electrostatic forces pull the electrodes together. If the voltage is high enough, the movable reflective layer 14 is deformed and is forced against the optical stack 16. A dielectric layer (not illustrated in this Figure) within the optical stack 16 may prevent shorting and control the separation distance between layers 14 and 16, as illustrated by pixel 12b on the right in Figure 1. The behavior is the same regardless of the polarity of the applied potential difference. In this way, row/column actuation that can control the reflective vs. non-reflective pixel states is analogous in many ways to that used in conventional LCD and other display technologies.

[0030] Figures 2 through 5B illustrate one exemplary process and system for using an array of interferometric modulators in a display application.

[0031] Figure 2 is a system block diagram illustrating one embodiment of an electronic device that may incorporate aspects of the invention. In the exemplary embodiment, the electronic device includes a processor 21 which may be any general purpose single- or multi-chip microprocessor such as an ARM, Pentium®, Pentium II®, Pentium III®, Pentium IV®, Pentium® Pro, an 8051, a MIPS®, a Power PC®, an ALPHA®, or any special purpose microprocessor such as a digital signal processor, microcontroller, or a programmable gate array. As is conventional in the art, the processor 21 may be configured to execute one or more software modules. In addition to executing an operating system, the processor may be configured to execute one or more software applications, including a web browser, a telephone application, an email program, or any other software application.

[0032] In one embodiment, the processor 21 is also configured to communicate with an array driver 22. In one embodiment, the array driver 22 includes a row driver circuit 24 and a column driver circuit 26 that provide signals to a display array or panel 30. The cross section of the array illustrated in Figure 1 is shown by the lines 1-1 in Figure 2. For MEMS interferometric modulators, the row/column actuation protocol may take advantage of a hysteresis property of these devices illustrated in Figure 3. It may require, for example, a 10 volt potential difference to cause a movable layer to deform from the relaxed state to the actuated state. However, when the voltage is reduced from that value, the movable layer maintains its state as the voltage drops back below 10 volts. In the exemplary embodiment of Figure 3, the movable layer does not relax completely until the voltage drops below 2 volts. There is thus a range of voltage, about 3 to 7 V in the example illustrated in Figure 3, where there exists a window of applied voltage within which the device is stable in either the relaxed or actuated state. This is referred to herein as the "hysteresis window" or "stability window." For a display array having the hysteresis characteristics of Figure 3, the row/column actuation protocol can be designed such that during row strobing, pixels in the strobed row that are to be actuated are exposed to a voltage difference of about 10 volts, and pixels that are to be relaxed are exposed to a voltage difference of close to zero volts. After the strobe, the pixels are exposed to a steady state voltage difference of about 5 volts such that they remain in whatever state the row strobe put them in. After being written, each pixel sees a potential difference within the "stability window" of 3-7 volts in this example. This

feature makes the pixel design illustrated in Figure 1 stable under the same applied voltage conditions in either an actuated or relaxed pre-existing state. Since each pixel of the interferometric modulator, whether in the actuated or relaxed state, is essentially a capacitor formed by the fixed and moving reflective layers, this stable state can be held at a voltage within the hysteresis window with almost no power dissipation. Essentially no current flows into the pixel if the applied potential is fixed.

[0033] In typical applications, a display frame may be created by asserting the set of column electrodes in accordance with the desired set of actuated pixels in the first row. A row pulse is then applied to the row 1 electrode, actuating the pixels corresponding to the asserted column lines. The asserted set of column electrodes is then changed to correspond to the desired set of actuated pixels in the second row. A pulse is then applied to the row 2 electrode, actuating the appropriate pixels in row 2 in accordance with the asserted column electrodes. The row 1 pixels are unaffected by the row 2 pulse, and remain in the state they were set to during the row 1 pulse. This may be repeated for the entire series of rows in a sequential fashion to produce the frame. Generally, the frames are refreshed and/or updated with new display data by continually repeating this process at some desired number of frames per second. A wide variety of protocols for driving row and column electrodes of pixel arrays to produce display frames are also well known and may be used in conjunction with the present invention.

[0034] Figures 4, 5A, and 5B illustrate one possible actuation protocol for creating a display frame on the 3x3 array of Figure 2. Figure 4 illustrates a possible set of column and row voltage levels that may be used for pixels exhibiting the hysteresis curves of Figure 3. In the Figure 4 embodiment, actuating a pixel involves setting the appropriate column to $-V_{\text{bias}}$, and the appropriate row to $+\Delta V$, which may correspond to -5 volts and +5 volts respectively. Relaxing the pixel is accomplished by setting the appropriate column to $+V_{\text{bias}}$, and the appropriate row to the same $+\Delta V$, producing a zero volt potential difference across the pixel. In those rows where the row voltage is held at zero volts, the pixels are stable in whatever state they were originally in, regardless of whether the column is at $+V_{\text{bias}}$, or $-V_{\text{bias}}$. As is also illustrated in Figure 4, it will be appreciated that voltages of opposite polarity than those described above can be used, e.g., actuating a pixel can involve setting the

appropriate column to $+V_{\text{bias}}$, and the appropriate row to $-\Delta V$. In this embodiment, releasing the pixel is accomplished by setting the appropriate column to $-V_{\text{bias}}$, and the appropriate row to the same $-\Delta V$, producing a zero volt potential difference across the pixel. As is also illustrated in Figure 4, it will be appreciated that voltages of opposite polarity than those described above can be used, e.g., actuating a pixel can involve setting the appropriate column to $+V_{\text{bias}}$, and the appropriate row to $-\Delta V$. In this embodiment, releasing the pixel is accomplished by setting the appropriate column to $-V_{\text{bias}}$, and the appropriate row to the same $-\Delta V$, producing a zero volt potential difference across the pixel.

[0035] Figure 5B is a timing diagram showing a series of row and column signals applied to the 3x3 array of Figure 2 which will result in the display arrangement illustrated in Figure 5A, where actuated pixels are non-reflective. Prior to writing the frame illustrated in Figure 5A, the pixels can be in any state, and in this example, all the rows are at 0 volts, and all the columns are at +5 volts. With these applied voltages, all pixels are stable in their existing actuated or relaxed states.

[0036] In the Figure 5A frame, pixels (1,1), (1,2), (2,2), (3,2) and (3,3) are actuated. To accomplish this, during a "line time" for row 1, columns 1 and 2 are set to -5 volts, and column 3 is set to +5 volts. This does not change the state of any pixels, because all the pixels remain in the 3-7 volt stability window. Row 1 is then strobed with a pulse that goes from 0, up to 5 volts, and back to zero. This actuates the (1,1) and (1,2) pixels and relaxes the (1,3) pixel. No other pixels in the array are affected. To set row 2 as desired, column 2 is set to -5 volts, and columns 1 and 3 are set to +5 volts. The same strobe applied to row 2 will then actuate pixel (2,2) and relax pixels (2,1) and (2,3). Again, no other pixels of the array are affected. Row 3 is similarly set by setting columns 2 and 3 to -5 volts, and column 1 to +5 volts. The row 3 strobe sets the row 3 pixels as shown in Figure 5A. After writing the frame, the row potentials are zero, and the column potentials can remain at either +5 or -5 volts, and the display is then stable in the arrangement of Figure 5A. It will be appreciated that the same procedure can be employed for arrays of dozens or hundreds of rows and columns. It will also be appreciated that the timing, sequence, and levels of voltages used to perform row and column actuation can be varied widely within the general

principles outlined above, and the above example is exemplary only, and any actuation voltage method can be used with the systems and methods described herein.

[0037] Figures 6A and 6B are system block diagrams illustrating an embodiment of a display device 40. The display device 40 can be, for example, a cellular or mobile telephone. However, the same components of display device 40 or slight variations thereof are also illustrative of various types of display devices such as televisions and portable media players.

[0038] The display device 40 includes a housing 41, a display 30, an antenna 43, a speaker 44, an input device 48, and a microphone 46. The housing 41 is generally formed from any of a variety of manufacturing processes as are well known to those of skill in the art, including injection molding, and vacuum forming. In addition, the housing 41 may be made from any of a variety of materials, including but not limited to plastic, metal, glass, rubber, and ceramic, or a combination thereof. In one embodiment the housing 41 includes removable portions (not shown) that may be interchanged with other removable portions of different color, or containing different logos, pictures, or symbols.

[0039] The display 30 of exemplary display device 40 may be any of a variety of displays, including a bi-stable display, as described herein. In other embodiments, the display 30 includes a flat-panel display, such as plasma, EL, OLED, STN LCD, or TFT LCD as described above, or a non-flat-panel display, such as a CRT or other tube device, as is well known to those of skill in the art. However, for purposes of describing the present embodiment, the display 30 includes an interferometric modulator display, as described herein.

[0040] The components of one embodiment of exemplary display device 40 are schematically illustrated in Figure 6B. The illustrated exemplary display device 40 includes a housing 41 and can include additional components at least partially enclosed therein. For example, in one embodiment, the exemplary display device 40 includes a network interface 27 that includes an antenna 43 which is coupled to a transceiver 47. The transceiver 47 is connected to a processor 21, which is connected to conditioning hardware 52. The conditioning hardware 52 may be configured to condition a signal (e.g. filter a signal). The conditioning hardware 52 is connected to a speaker 45 and a microphone 46. The processor

21 is also connected to an input device 48 and a driver controller 29. The driver controller 29 is coupled to a frame buffer 28, and to an array driver 22, which in turn is coupled to a display array 30. A power supply 50 provides power to all components as required by the particular exemplary display device 40 design.

[0041] The network interface 27 includes the antenna 43 and the transceiver 47 so that the exemplary display device 40 can communicate with one or more devices over a network. In one embodiment the network interface 27 may also have some processing capabilities to relieve requirements of the processor 21. The antenna 43 is any antenna known to those of skill in the art for transmitting and receiving signals. In one embodiment, the antenna transmits and receives RF signals according to the IEEE 802.11 standard, including IEEE 802.11(a), (b), or (g). In another embodiment, the antenna transmits and receives RF signals according to the BLUETOOTH standard. In the case of a cellular telephone, the antenna is designed to receive CDMA, GSM, AMPS or other known signals that are used to communicate within a wireless cell phone network. The transceiver 47 pre-processes the signals received from the antenna 43 so that they may be received by and further manipulated by the processor 21. The transceiver 47 also processes signals received from the processor 21 so that they may be transmitted from the exemplary display device 40 via the antenna 43.

[0042] In an alternative embodiment, the transceiver 47 can be replaced by a receiver. In yet another alternative embodiment, network interface 27 can be replaced by an image source, which can store or generate image data to be sent to the processor 21. For example, the image source can be a digital video disc (DVD) or a hard-disc drive that contains image data, or a software module that generates image data.

[0043] Processor 21 generally controls the overall operation of the exemplary display device 40. The processor 21 receives data, such as compressed image data from the network interface 27 or an image source, and processes the data into raw image data or into a format that is readily processed into raw image data. The processor 21 then sends the processed data to the driver controller 29 or to frame buffer 28 for storage. Raw data typically refers to the information that identifies the image characteristics at each location

within an image. For example, such image characteristics can include color, saturation, and gray-scale level.

[0044] In one embodiment, the processor 21 includes a microcontroller, CPU, or logic unit to control operation of the exemplary display device 40. Conditioning hardware 52 generally includes amplifiers and filters for transmitting signals to the speaker 45, and for receiving signals from the microphone 46. Conditioning hardware 52 may be discrete components within the exemplary display device 40, or may be incorporated within the processor 21 or other components.

[0045] The driver controller 29 takes the raw image data generated by the processor 21 either directly from the processor 21 or from the frame buffer 28 and reformats the raw image data appropriately for high speed transmission to the array driver 22. Specifically, the driver controller 29 reformats the raw image data into a data flow having a raster-like format, such that it has a time order suitable for scanning across the display array 30. Then the driver controller 29 sends the formatted information to the array driver 22. Although a driver controller 29, such as a LCD controller, is often associated with the system processor 21 as a stand-alone Integrated Circuit (IC), such controllers may be implemented in many ways. They may be embedded in the processor 21 as hardware, embedded in the processor 21 as software, or fully integrated in hardware with the array driver 22.

[0046] Typically, the array driver 22 receives the formatted information from the driver controller 29 and reformats the video data into a parallel set of waveforms that are applied many times per second to the hundreds and sometimes thousands of leads coming from the display's x-y matrix of pixels.

[0047] In one embodiment, the driver controller 29, array driver 22, and display array 30 are appropriate for any of the types of displays described herein. For example, in one embodiment, driver controller 29 is a conventional display controller or a bi-stable display controller (e.g., an interferometric modulator controller). In another embodiment, array driver 22 is a conventional driver or a bi-stable display driver (e.g., an interferometric modulator display). In one embodiment, a driver controller 29 is integrated with the array driver 22. Such an embodiment is common in highly integrated systems such as cellular phones, watches, and other small area displays. In yet another embodiment, display array 30

is a typical display array or a bi-stable display array (e.g., a display including an array of interferometric modulators).

[0048] The input device 48 allows a user to control the operation of the exemplary display device 40. In one embodiment, input device 48 includes a keypad, such as a QWERTY keyboard or a telephone keypad, a button, a switch, a touch-sensitive screen, a pressure- or heat-sensitive membrane. In one embodiment, the microphone 46 is an input device for the exemplary display device 40. When the microphone 46 is used to input data to the device, voice commands may be provided by a user for controlling operations of the exemplary display device 40.

[0049] Power supply 50 can include a variety of energy storage devices as are well known in the art. For example, in one embodiment, power supply 50 is a rechargeable battery, such as a nickel-cadmium battery or a lithium ion battery. In another embodiment, power supply 50 is a renewable energy source, a capacitor, or a solar cell, including a plastic solar cell, and solar-cell paint. In another embodiment, power supply 50 is configured to receive power from a wall outlet.

[0050] In some implementations control programmability resides, as described above, in a driver controller which can be located in several places in the electronic display system. In some cases control programmability resides in the array driver 22. Those of skill in the art will recognize that the above-described optimization may be implemented in any number of hardware and/or software components and in various configurations.

[0051] The details of the structure of interferometric modulators that operate in accordance with the principles set forth above may vary widely. For example, Figures 7A-7E illustrate five different embodiments of the movable reflective layer 14 and its supporting structures. Figure 7A is a cross section of the embodiment of Figure 1, where a strip of metal material 14 is deposited on orthogonally extending supports 18. In Figure 7B, the moveable reflective layer 14 is attached to supports at the corners only, on tethers 32. In Figure 7C, the moveable reflective layer 14 is suspended from a deformable layer 34, which may comprise a flexible metal. The deformable layer 34 connects, directly or indirectly, to the substrate 20 around the perimeter of the deformable layer 34. These connections are herein referred to as support posts. The embodiment illustrated in Figure 7D has support post plugs 42 upon

which the deformable layer 34 rests. The movable reflective layer 14 remains suspended over the cavity, as in Figures 7A-7C, but the deformable layer 34 does not form the support posts by filling holes between the deformable layer 34 and the optical stack 16. Rather, the support posts are formed of a planarization material, which is used to form support post plugs 42. The embodiment illustrated in Figure 7E is based on the embodiment shown in Figure 7D, but may also be adapted to work with any of the embodiments illustrated in Figures 7A-7C as well as additional embodiments not shown. In the embodiment shown in Figure 7E, an extra layer of metal or other conductive material has been used to form a bus structure 44. This allows signal routing along the back of the interferometric modulators, eliminating a number of electrodes that may otherwise have had to be formed on the substrate 20.

[0052] In embodiments such as those shown in Figure 7, the interferometric modulators function as direct-view devices, in which images are viewed from the front side of the transparent substrate 20, the side opposite to that upon which the modulator is arranged. In these embodiments, the reflective layer 14 optically shields the portions of the interferometric modulator on the side of the reflective layer opposite the substrate 20, including the deformable layer 34. This allows the shielded areas to be configured and operated upon without negatively affecting the image quality. Such shielding allows the bus structure 44 in Figure 7E, which provides the ability to separate the optical properties of the modulator from the electromechanical properties of the modulator, such as addressing and the movements that result from that addressing. This separable modulator architecture allows the structural design and materials used for the electromechanical aspects and the optical aspects of the modulator to be selected and to function independently of each other. Moreover, the embodiments shown in Figures 7C-7E have additional benefits deriving from the decoupling of the optical properties of the reflective layer 14 from its mechanical properties, which are carried out by the deformable layer 34. This allows the structural design and materials used for the reflective layer 14 to be optimized with respect to the optical properties, and the structural design and materials used for the deformable layer 34 to be optimized with respect to desired mechanical properties.

[0053] As discussed above, the interferometric modulator is configured to reflect light through the transparent substrate and includes moving parts, such as the movable

mirrors 14a, 14b. Therefore, to allow such moving parts to move, a gap or cavity is preferably created to allow the mechanical parts, such as the movable mirrors 14a, 14b, of the interferometric modulator to move.

[0054] Figures 8A-8C are cross-sectional views of an interferometric modulator formed on a pre-patterned substrate, in accordance with an embodiment. It has been found that the steps involved in producing interferometric modulators that function as described above are adaptable to very cost effective production techniques when a pre-patterned substrate is used. Figures 8A-8C illustrate one embodiment of such a method, which results in an interferometric modulator that is viewed from the opposite side as the interferometric modulator described above with reference to Figures 1-7E. Depending on the final application of the device, it will sometimes be preferable to produce a display viewed through the substrate, and sometimes preferable to produce a display viewed through the deposited layers of the interferometric modulator. Thus, with such a design, it is not necessary to use a transparent substrate (such as transparent substrate 20, shown in Figures 7A-7E) on which to form the interferometric modulator. The pre-patterned substrate may therefore be either opaque or transparent. In the illustrated embodiment shown in Figures 8A-8C, the pre-patterned substrate is preferably non-transparent, which allows for a selection of materials that are conducive to embossing.

[0055] According to the embodiment shown in Figures 8A-8C, an interferometric modulator is formed on a pre-patterned substrate 505. A substrate 505 having trenches 507 formed therein is preferably covered with a mirror layer to form a lower electrode (a semi-reflecting or reflecting means) 502, which will serve as the fixed layer described above.

[0056] The substrate 505 may be formed from a preferably non-transparent polymer material having a series of embossed, appropriately spaced grooves or trenches 507 running in one direction along the substrate surface. These grooves 507 may be embossed using known techniques in a variety of conventional materials to preferably have a reentrant profile with tapering sides, as shown in Figures 8A-8C. In a preferred embodiment, a substrate 505 formed of a composite material is embossed, stamped, ablated, molded, or mechanically imprinted with trenches or grooves, and subsequently baked to obtain the reentrant profile. The skilled artisan will appreciate that such a composite material is

preferably formed of different materials in different layers and that after stamping or embossing the substrate, baking causes differential thermal expansion in the different layers. In the illustrated embodiment, the top layer(s) have a higher coefficient of thermal expansion, thereby causing expansion into the grooves 507. Although the reentrant profile is preferred, the skilled artisan will appreciate that the grooves 507 may have other shapes (e.g., vertical walls) so long as the grooves cause a break in material deposited over the top surface of the substrate 505, as discussed in more detail below. It will be understood that the grooves 507 may be formed in the substrate 505 by techniques other than embossing, such as, for example, etching. However, embossing or stamping is preferred as it is an inexpensive process.

[0057] When material is deposited on such a surface structure, some material will be deposited and settle into the grooves 507, and some material will be deposited and settle on the top surface of the substrate 505 between the grooves 507. The material is preferably deposited by conventional deposition techniques, such as some form of sputtering, physical vapor deposition, and chemical vapor deposition (CVD). As shown in Figures 8A-8C, the presence of the grooves 507 produces breaks or discontinuities in the deposited layers on the top surface of the substrate 505. In this way, the lower layers 502, 508, 510 of the interferometric modulator structure are deposited without the conventional photolithography and etching steps. In this embodiment, effectively, the first set of masks that produces the structure of Figures 7A-7E is incorporated into the substrate 505 itself, and conventional masking can, in fact, be replaced by economical embossing processes for the initial electrode pattern. According to this embodiment, the first steps of interferometric modulator structure fabrication are thus depositing the lower electrode 502, a dielectric material 508, and a layer of sacrificial material 510. The layers of the deposited lower electrode 502, dielectric material 508, and sacrificial material 510 are thus formed in rows or strips on the top surface of the substrate 505. The strip structure is produced naturally by the presence of the embossed grooves 507.

[0058] The lower electrode 502 is preferably formed of aluminum. In other embodiments, the lower electrode 502 may comprise other highly reflective metals, such as,

for example, silver (Ag) or gold (Au). Alternatively, the lower electrode 502 may be a stack of metals configured to give the proper optical and mechanical properties.

[0059] A dielectric layer 508 is preferably deposited over the lower electrode 502. In a preferred embodiment, the dielectric material is silicon dioxide (SiO_2). A sacrificial layer 510 is preferably deposited (and later removed) over the structure to create a resonant optical cavity between the lower electrode 502 and an upper electrode or reflecting means 506 that will be deposited over the sacrificial layer 510 to form the movable layer, as shown in Figure 8B. In the illustrated embodiment, the sacrificial layer 510 comprises silicon (Si). In other embodiments, this sacrificial layer 510 may be formed of molybdenum (Mo), tungsten (W), or titanium (Ti). All of these sacrificial materials can be selectively etched, relative to the exposed dielectric and electrode materials, but the skilled artisan will readily appreciate that other sacrificial materials (*e.g.*, photoresist) can be used with other selective etch chemistries.

[0060] As shown in Figure 8B, in this embodiment, interferometric modulator structure production is continued by filling the trenches 507 and the regions between the previously deposited structures. This filling can be done with many conventional deposition/pattern/etch steps or with an etch back process, such as chemical mechanical polishing (CMP) planarization step, for example.

[0061] Orthogonal upper electrode strips 506 are preferably deposited over the sacrificial layer 510, followed by strips of a second or upper sacrificial material 520 separated by posts 522. This upper electrode 506 is deposited as strips in rows orthogonal to the lower electrode 502 rows to create the row/column array described above. The upper electrode 506 and sacrificial material 520 may be deposited as strips in their desired patterns, preferably using a shadow mask deposition technique. The posts 522 are formed of insulating materials, preferably a polymer or dielectric material.

[0062] A thin, preferably 50-100 angstrom, semi-reflective layer 530 is then preferably deposited over the upper sacrificial layer 520. In a preferred embodiment, the semi-reflective layer 530 is chromium. As shown in Figure 8B, a transparent material or viewing means 535 is deposited over the semi-reflective layer 530 to provide additional mechanical and structural integrity to the semi-reflective layer 530, which is typically too thin

to support itself after removal of the sacrificial layers 510, 520. The skilled artisan will understand that the transparent substrate 535 serves a mechanical function as well as a means through which display takes place and through which light is transmitted. The transparent substrate 535 may be formed of a solid inorganic material, such as an oxide. In another embodiment, the transparent substrate 535 may be formed of a transparent polymer. The semi-reflective layer 530 and the transparent substrate are preferably deposited by conventional deposition techniques, such as sputtering, PVD, and CVD.

[0063] The transparent material 535 and semi-reflective layer 530 are preferably etched with openings or holes (not shown) so that the etch gas used for sacrificial layer removal can reach the sacrificial material of layers 510 and 520. Alternatively, the transparent material 535 may be pre-patterned with openings or holes that are pre-etched or embossed. It will be understood that, as part of the overall packaging process, the interferometric modulators are sealed and protected from the environment surrounding the package containing the interferometric modulators. Preferably, the holes or openings have a diameter as small as the photolithographic system will permit, and more preferably about 2.4 microns. The skilled artisan will understand that the size, spacing, and number of openings will affect the rate of removal of the sacrificial layers 510, 520.

[0064] The sacrificial layers 510, 520 are removed, preferably using a selective gas etching process, to create the optical cavity around the movable electrode 506. Standard etching techniques may be used to remove the sacrificial layers 510, 520. The particular gas etching process will depend on the material to be removed. For example, xenon difluoride (XeF_2) may be used as the release gas for removing a silicon sacrificial layer. It will be understood that the etching process is a selective etching process that does not etch any dielectric, semi-reflecting, or electrode materials.

[0065] The final structure of the interferometric modulator is shown in Figure 8C, where there is an optical cavity surrounding the moving electrode 506. Because the semi-reflective layer 530 is on top, the interferometric modulator is viewed through the transparent substrate 535 from the side of the deposited layers in the direction of arrow 540, as shown in Figure 8C.

[0066] In the embodiment shown in Figure 8A-8C, it will be understood that the movable layer 506 of the interferometric modulator is adjacent the transparent substrate 535 and the fixed layer 502 is formed below the movable layer 506 such that the movable layer 506 may move within the optical cavity of the structure, as shown in Figure 8C.

[0067] The skilled artisan will appreciate that, in the embodiment shown in Figure 8D, the semi-reflective layer 530 is preferably chromium and can be supplemented with a transparent electrode, preferably an ITO layer, and used as an electrode. As shown in Figure 8D, the ITO layer 532 is between the transparent substrate 535 and the chromium layer 530. This ITO-chromium bilayer structure eliminates the need for the lower electrode 502 and dielectric 508 of the embodiment shown in Figures 8A-8C. In this embodiment, a dielectric layer 508 is between the chromium 530 and the upper cavity. The skilled artisan will appreciate that a first sacrificial layer (not shown) is deposited on the pre-patterned transparent substrate 505 and later removed to form a lower cavity 560, and a second sacrificial layer (not shown) is deposited over the electrode 506 to form the upper cavity 565. As shown in Figure 8D, the electrode 506 is deposited over the pre-patterned substrate 505 forming electrode strips on the top surface of the substrate 505, where the discontinuities in the electrode 506 are caused by deposition over the trenches 507.

[0068] As mentioned above, transparent pre-patterned substrates made from transparent materials, such as polymers, may be used to create an interferometric modulator similar to that shown in Figures 7A-7E. In such an interferometric modulator, as shown in Figure 8E, unlike the embodiment shown in Figures 8A-8C, the transparent pre-patterned substrate 580 transmits light and viewing takes place through the transparent pre-patterned substrate 580. The skilled artisan will understand that the process for making such an interferometric modulator is similar to the method described above with reference to Figures 8A-8C, but that the electrode structure would be reversed. The structure would be similar to that of Figures 7A-7E, but the first few patterning and etching steps to create the rows are eliminated by depositing the first layers in rows over the trenches 507.

[0069] As shown in Figure 8E, the semi-reflective-ITO bilayer 530, 532 is deposited over the substrate 580 to form electrode strips. A dielectric layer 508 is deposited over the semi-reflective-ITO bilayer 530, 532. A first sacrificial layer (not shown) is then

deposited and later removed to form a lower cavity 560. The movable electrode 506 is deposited in orthogonal strips over the first sacrificial layer. A second sacrificial layer (not shown) is deposited over the movable electrode 506 and later removed to form the upper cavity 565. As shown in Figure 8E, the movable electrode 506 is in a collapsed state. To complete the structure, a deformable layer 570 is formed over the upper cavity 565.

[0070] While the above detailed description has shown, described, and pointed out novel features of the invention as applied to various embodiments, it will be understood that various omissions, substitutions, and changes in the form and details of the device or process illustrated may be made by those skilled in the art without departing from the spirit of the invention. As will be recognized, the present invention may be embodied within a form that does not provide all of the features and benefits set forth herein, as some features may be used or practiced separately from others.

WHAT IS CLAIMED IS:

1. A method of making a microelectromechanical systems device, comprising:
providing a substrate having a plurality of trenches;
depositing at least one layer over the substrate, wherein the layer is discontinuous at the trenches; and
creating a first cavity between a first electrode formed over the substrate and a second electrode, wherein the at least one layer comprises the first electrode.
2. The method of Claim 1, further comprising insulating the first electrode from the second electrode.
3. The method of Claim 2, wherein insulating comprises depositing a dielectric material over the first electrode.
4. The method of Claim 3, wherein the first cavity is created by depositing a sacrificial material over the dielectric material prior to forming the second electrode and removing the sacrificial material after forming the second electrode.
5. The method of Claim 1, further comprising creating a second cavity between the second electrode and a semi-reflective layer.
6. The method of Claim 5, wherein creating the second cavity comprises depositing a sacrificial material over the second electrode prior to forming the semi-reflective layer and removing the sacrificial material after forming the semi-reflective layer.
7. The method of Claim 5, wherein the semi-reflective layer comprises chromium.
8. The method of Claim 5, further comprising depositing a transparent material over the semi-reflective layer.

9. The method of Claim 8, wherein creating the first cavity comprises depositing a sacrificial material over the dielectric material prior to forming the second electrode and removing the sacrificial material after forming the second electrode.

10. The method of Claim 9, wherein the transparent material and semi-reflective layer are etched with openings prior to removing the first sacrificial layer.

11. The method of Claim 1, wherein providing comprises embossing the trenches in the substrate.

12. The method of Claim 1, wherein providing comprises ablating the trenches in the substrate.

13. The method of Claim 1, wherein providing comprises forming the trenches in the substrate by a molding process.

14. The method of Claim 1, wherein the at least one layer includes a conductive layer and the discontinuity of the layer at the trenches patterns the first electrode.

15. The method of Claim 1, wherein the microelectromechanical systems device is an interferometric modulator.

16. A microelectromechanical systems device formed by the method of Claim 1.

17. A display device, comprising:

a substrate embossed having a plurality of grooves formed therein;

a first electrode formed over a top surface of the substrate and a second electrode, wherein the first electrode and the second electrode are insulated from each other and separated by a first cavity;

a semi-reflective layer separated from the second electrode by a second cavity;

and

a transparent material formed over the semi-reflective layer.

18. The display device of Claim 17, wherein the first and second electrodes are insulated from each other by a dielectric material deposited over the first electrode.
19. The display device of Claim 17, wherein the transparent material has a plurality of holes formed therein.
20. The display device of Claim 17, wherein each of the plurality of grooves has a reentrant profile.
21. The display device of Claim 17, wherein the substrate is opaque.
22. The display device of Claim 17, wherein the first electrode is orthogonal to the second electrode.
23. The display device of Claim 17, wherein the second electrode is configured to be movable.
24. The display device of Claim 23, wherein the second cavity is larger and the first cavity is smaller when the second electrode is moved.
25. The display device of claim 17, further comprising:
 - a processor that is in electrical communication with at least one of said first and second electrodes, said processor being configured to process image data; and
 - a memory device in electrical communication with said processor
26. The display device of Claim 25, further comprising a driver circuit configured to send at least one signal to at least one of said first and second electrodes
27. The display device of Claim 26, further comprising a controller configured to send at least a portion of said image data to said driver circuit.

28. The display system of claim 25, further comprising an image source module configured to send said image data to said processor.

29. The display system of claim 28, wherein said image source module comprises at least one of a receiver, transceiver, and transmitter.

30. The display system of claim 25, further comprising an input device configured to receive input data and to communicate said input data to said processor.

31. A method of forming a microelectromechanical systems device, comprising:
providing a substrate having a top surface, wherein a plurality of grooves is formed in the top surface;

depositing at least one layer over the substrate, wherein the at least one layer comprises a first conductive material and is discontinuous at the grooves forming rows of the layer between the grooves on the top surface; and

depositing a second conductive material, wherein the second conductive material is oriented orthogonally to the first conductive material on the top surface.

32. The method of Claim 31, wherein the at least one layer further comprises a dielectric layer and a sacrificial material.

33. The method of Claim 31, further comprising creating a first cavity between the first conductive material and the second conductive material.

34. The method of Claim 32, further comprising depositing a semi-reflective layer over the second conductive material, wherein the semi-reflective layer and the second conductive material are separated by a cavity.

35. The method of Claim 33, wherein the cavity between the chromium layer and the second conductive material is created by removing a sacrificial material between the semi-reflective layer and the second conductive material.

36. The method of Claim 31, wherein providing the plurality of grooves in the top surface comprises embossing.

37. The method of Claim 36, further comprising baking the substrate after embossing to provide each of the grooves with a reentrant profile, wherein the substrate is formed of a composite material.

38. The method of Claim 37, wherein a top layer of the composite material has a higher coefficient of thermal expansion than that of a lower layer of the composite material.

39. A display device, comprising
a substrate means for causing discontinuous deposition thereover;
a first reflecting means for reflecting light formed over a top surface of the substrate and a second reflecting means for reflecting light, wherein the first reflecting means and the second reflecting means are insulated from each other and separated by a first separating means;
a semi-reflecting means separated from the second reflecting means by a second separating means; and
a viewing means for transmitting light, the viewing means formed over the semi-reflecting means.

40. The display device of Claim 39, wherein the substrate means include a groove whose shape is such that a layer deposited directly thereover is discontinuous over sidewalls of the groove.

41. The display device of Claim 39, wherein the substrate means include a trench shaped such that a layer deposited directly thereover is discontinuous over sidewalls of the trench.

42. The display device of Claim 39, wherein the first reflecting means on the top surface of the substrate means is a discontinuous layer forming rows of the first reflecting means on the top surface.

43. The display device of Claim 39, wherein the second reflecting means is a second reflecting layer hanging from a connector attached to a mechanical layer.

44. The display device of Claim 39, further comprising an insulating means between the first and second reflecting means, wherein the insulating means is a dielectric layer over the first reflecting means.

45. The display device of Claim 39, wherein the second reflecting means is a movable reflecting layer.

46. A method of operating a display device, comprising:
providing a substrate having a plurality of trenches, wherein at least one layer comprising a first electrode is formed over the substrate and wherein the layer is discontinuous at the trenches, and wherein the first electrode is separated from a second electrode by a cavity; and
moving the second electrode within the cavity.

47. The method of Claim 46, wherein moving the second electrode comprises moving the second electrode between a relaxed position and an actuated position.

48. The method of Claim 46, wherein moving the second electrode alters a distance between the first and second electrodes.

49. The method of Claim 46, wherein moving the second electrode comprises applying electrostatic attraction between the first and second electrodes.

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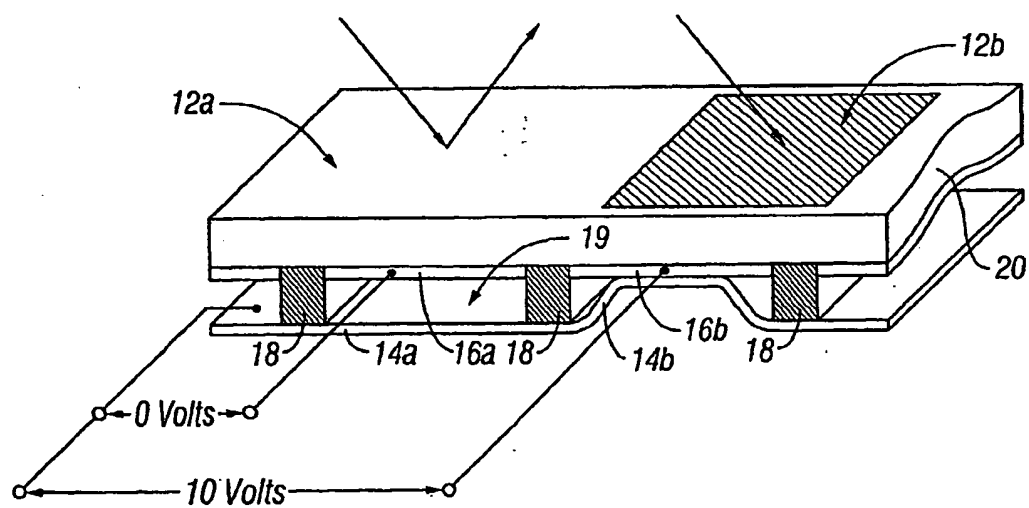


FIG. 1

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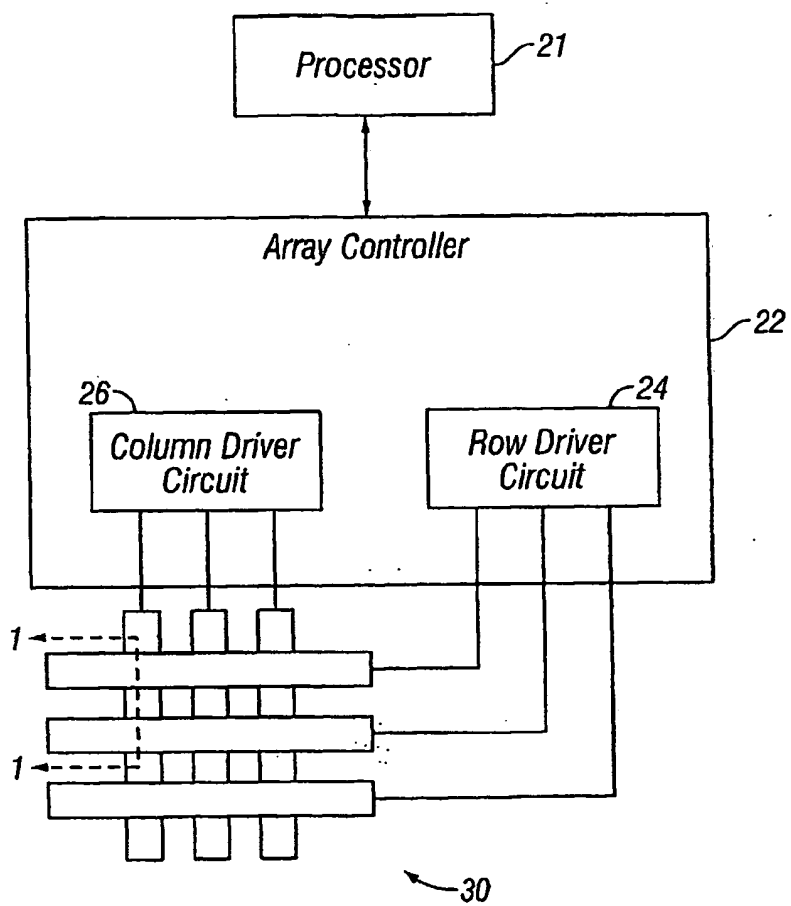


FIG. 2

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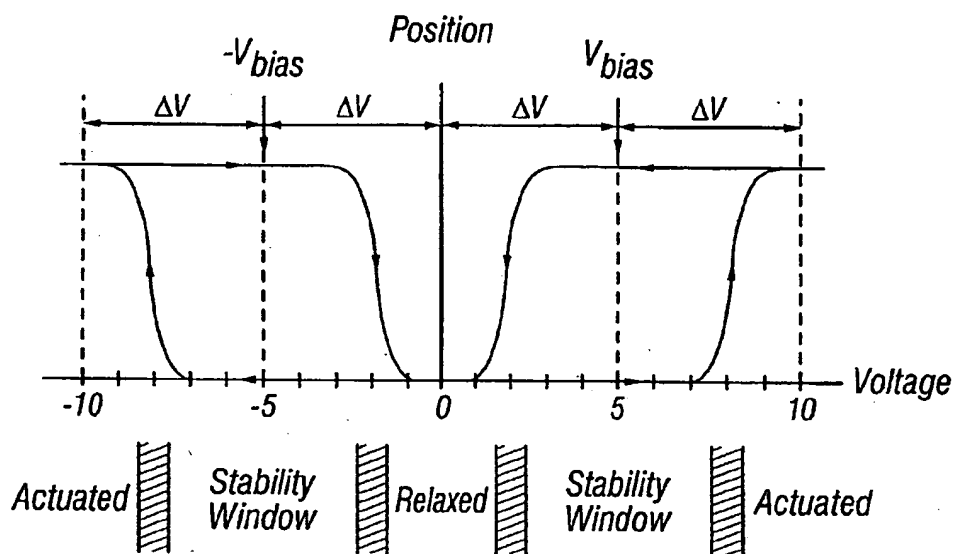


FIG. 3

		Column Output Signals	
		$+V_{bias}$	$-V_{bias}$
Row Output Signals	0	Stable	Stable
	$+\Delta V$	Relax	Actuate
	$-\Delta V$	Actuate	Relax

FIG. 4

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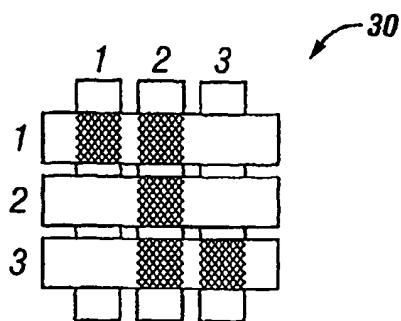


FIG. 5A

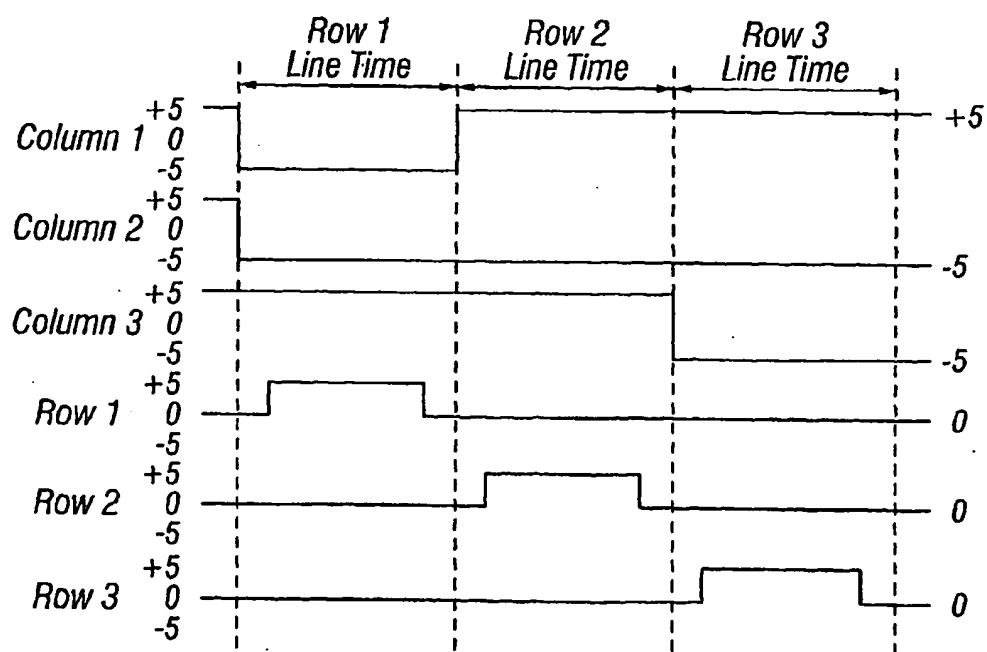


FIG. 5B

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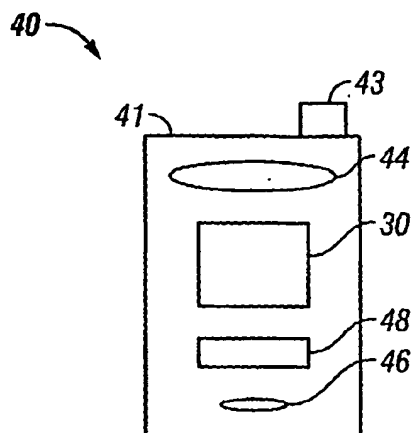


FIG. 6A

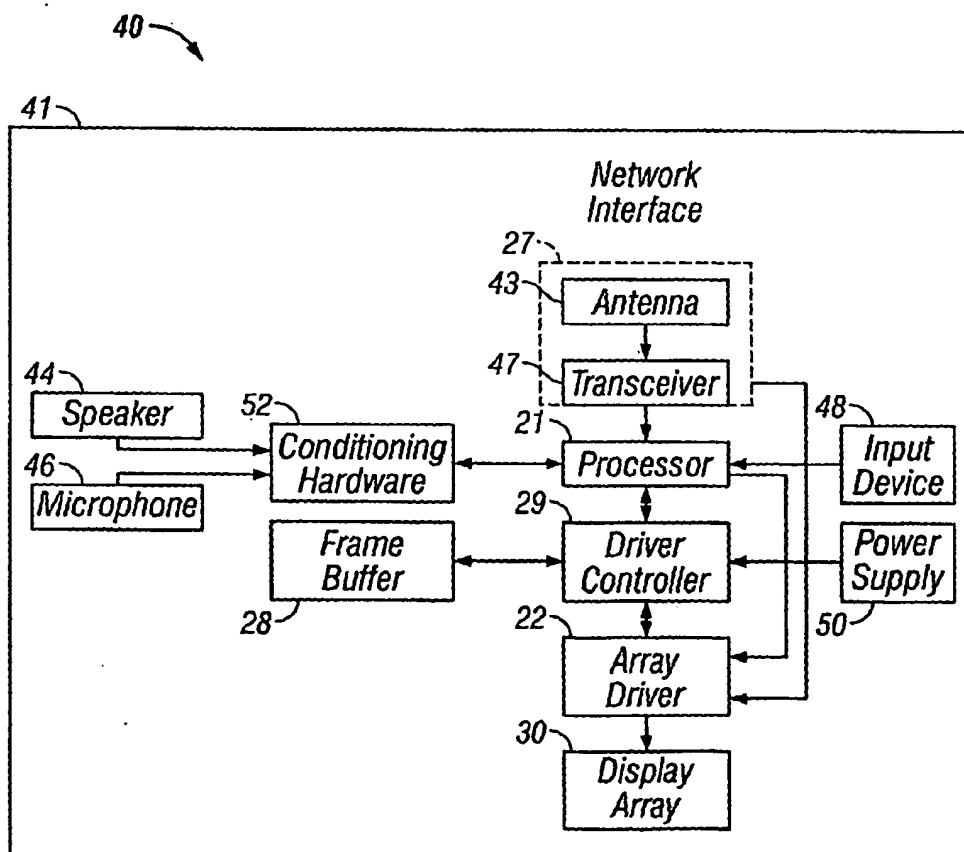


FIG. 6B

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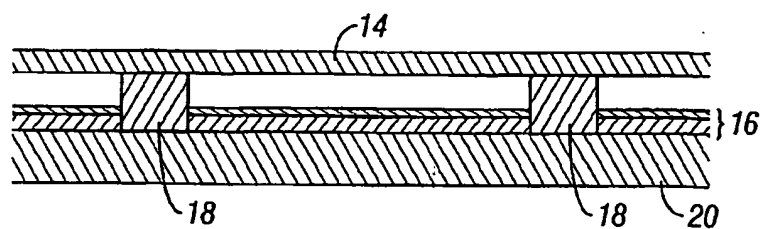


FIG. 7A

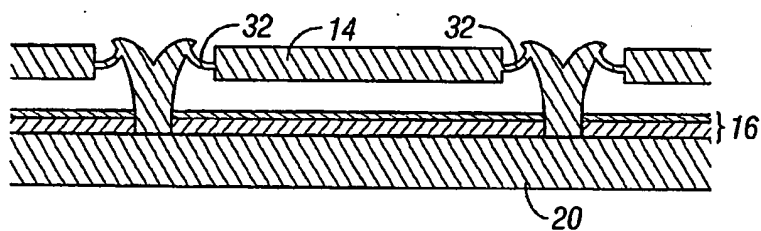


FIG. 7B

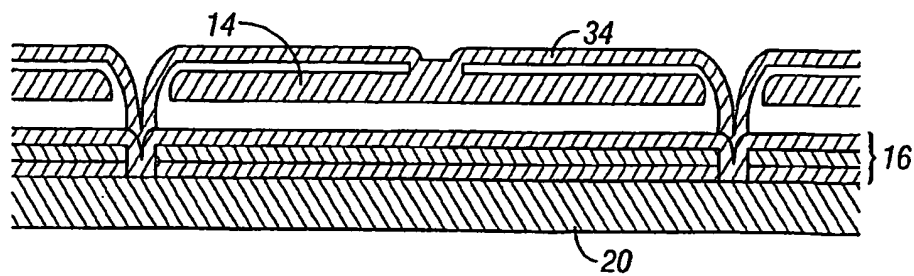


FIG. 7C

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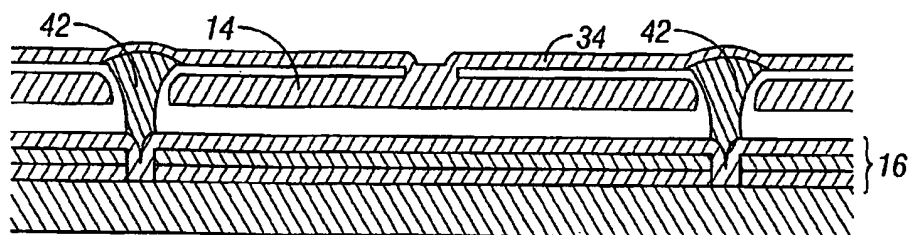


FIG. 7D

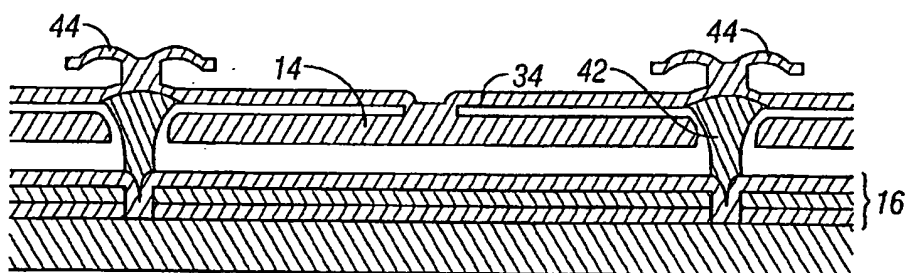


FIG. 7E

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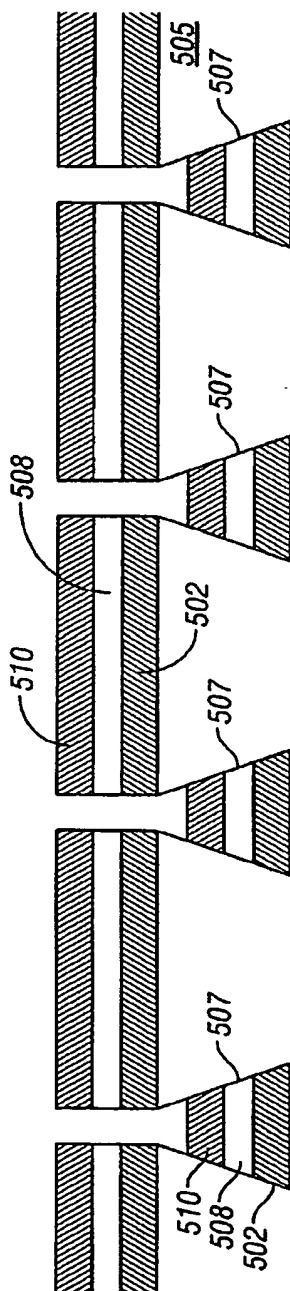


FIG. 8A

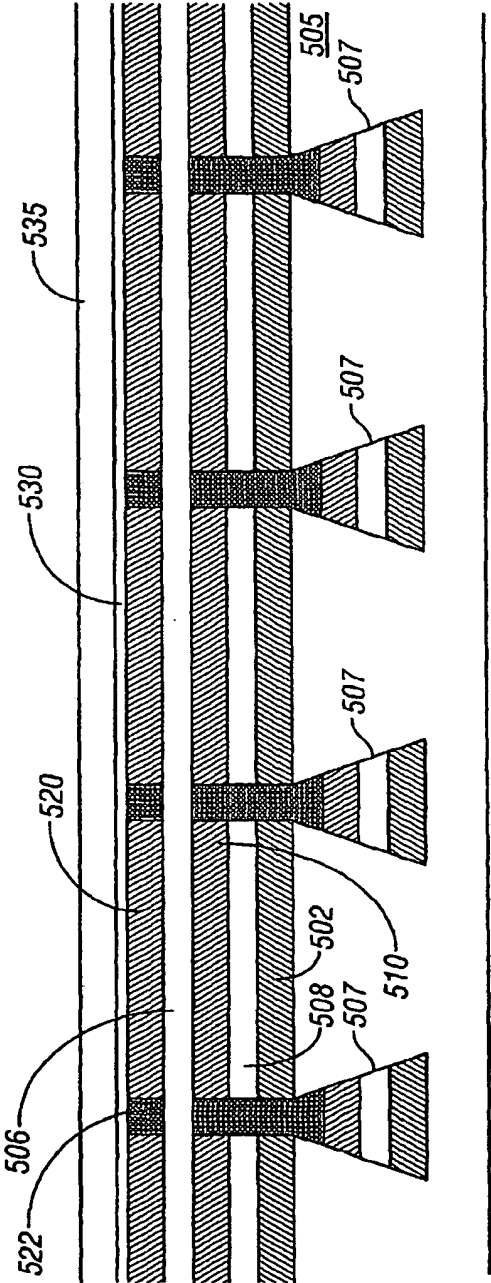


FIG. 8B

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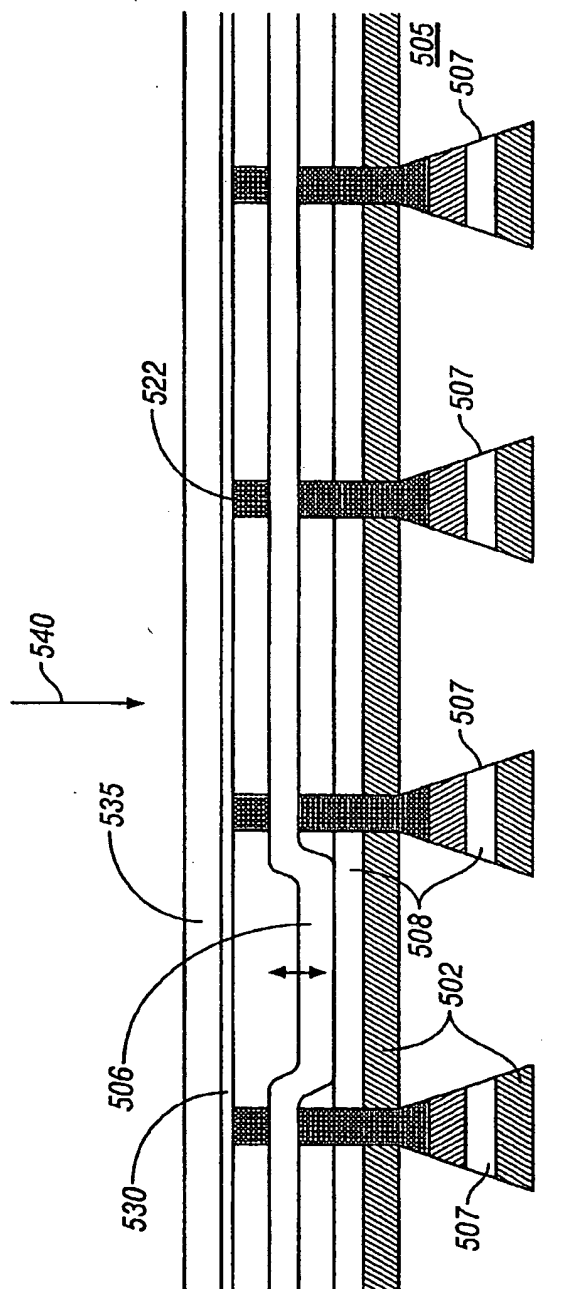


FIG. 8C.

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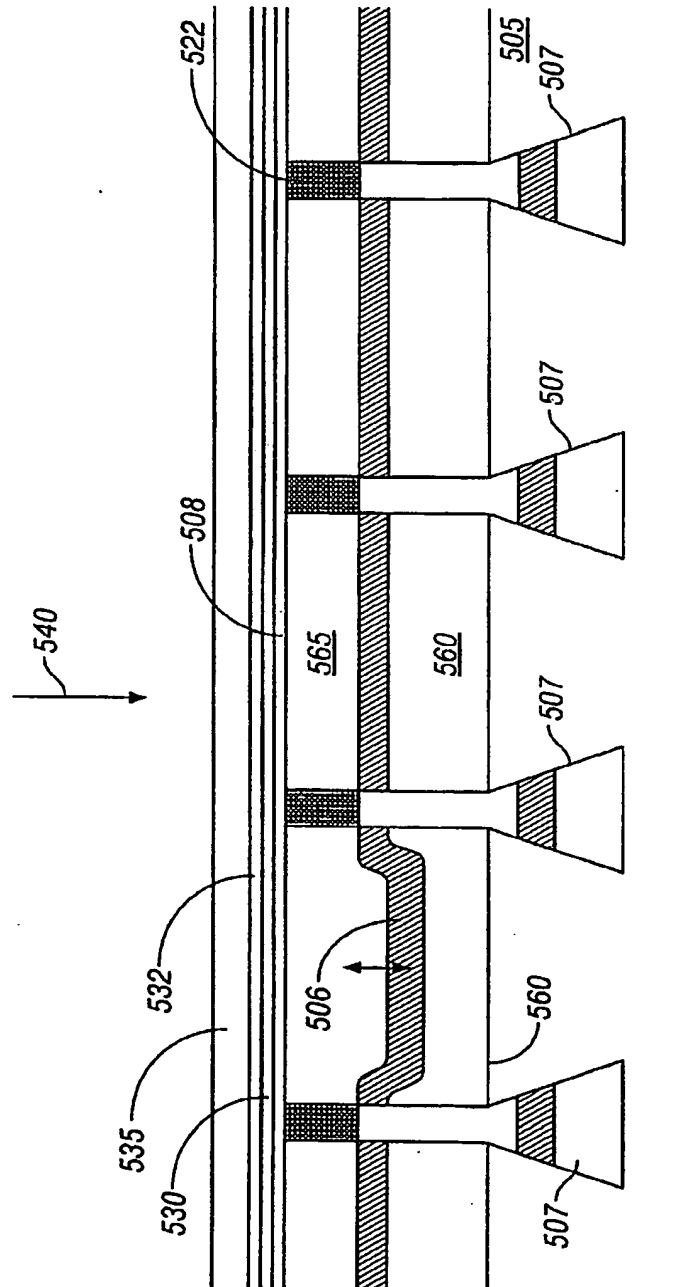


FIG. 8D

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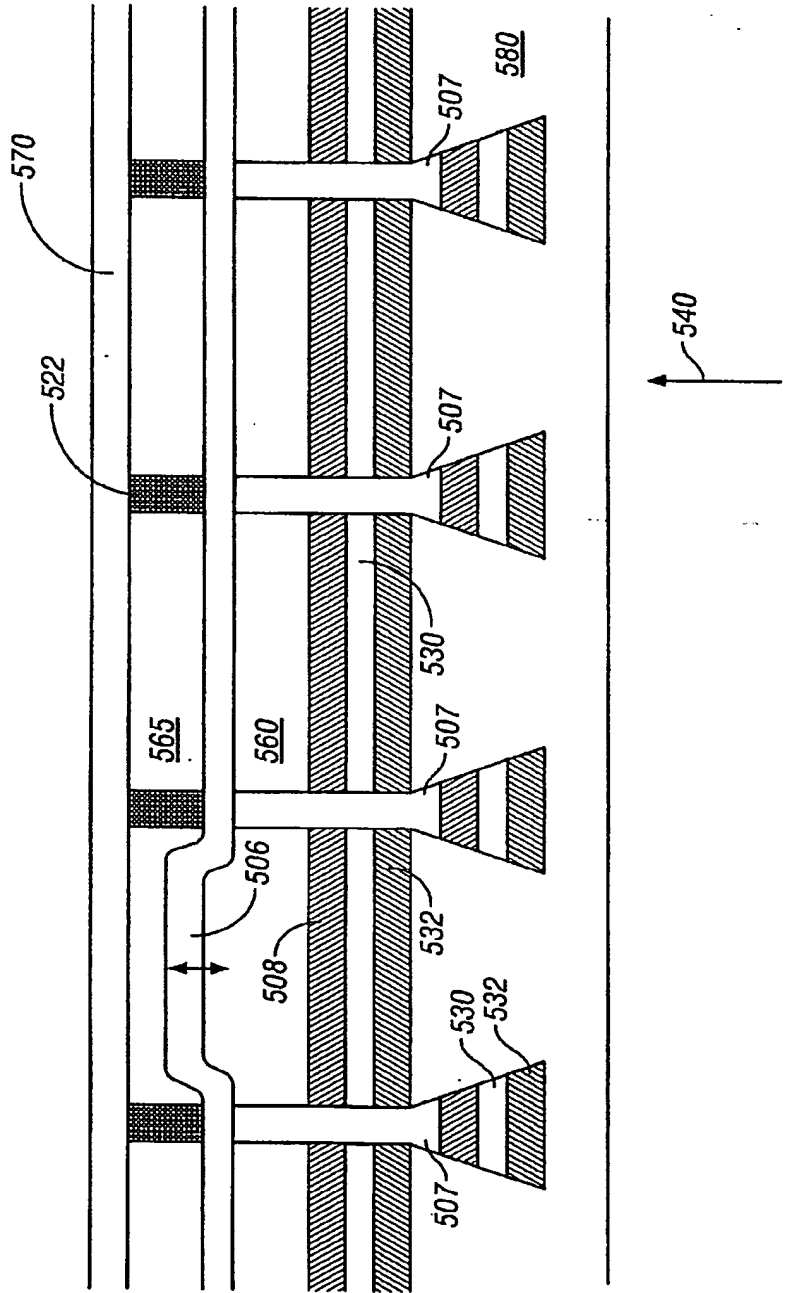


FIG. 8E

INTERNATIONAL SEARCH REPORT

International Application No
PC 1, US2005/029821

A. CLASSIFICATION OF SUBJECT MATTER 602B26/00		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) G02B B81C H01L		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2002/137072 A1 (MIRKIN CHAD A ET AL) 26 September 2002 (2002-09-26) figure 6 paragraphs '0036!', '0037!'	1-49
A	US 2002/015215 A1 (MILES MARK W) 7 February 2002 (2002-02-07) figure 3 paragraphs '0049!', '0050!'	5, 17
A	US 2002/055253 A1 (RUDHARD JOACHIM) 9 May 2002 (2002-05-09) figure 4 paragraph '0019!'	1
-/--		
<input checked="" type="checkbox"/> Further documents are listed in the continuation of box C. <input checked="" type="checkbox"/> Patent family members are listed in annex.		
* Special categories of cited documents : "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. "G" document member of the same patent family		
Date of the actual completion of the international search 8 December 2005		Date of mailing of the international search report 27/12/2005
Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax (+31-70) 340-3016		Authorized officer Luck, W

INTERNATIONAL SEARCH REPORT

International Application No
PCT/US2005/029821

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	WO 02/24570 A (MARCONI CASWELL LIMITED; BRIERLEY, CROFTON, JOHN; NEEDHAM, ANTHONY, PA) 28 March 2002 (2002-03-28) figure 2 abstract	5,17
A	CH 681 047 A5 (LANDIS & GYR BETRIEBS AG) 31 December 1992 (1992-12-31) abstractfigure 3	5,17
A	US 5 559 358 A (BURNS ET AL) 24 September 1996 (1996-09-24) abstractfigure 1	5,17

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No
PCT/US2005/029821

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2002137072 A1	26-09-2002	US 2003059777 A1	27-03-2003
		US 2003049630 A1	13-03-2003
		US 2002146720 A1	10-10-2002
		US 2002164605 A1	07-11-2002
		US 2002182611 A1	05-12-2002
		US 2002155458 A1	24-10-2002
		US 2002137070 A1	26-09-2002
		US 2002127574 A1	12-09-2002
		US 2002137071 A1	26-09-2002
		US 2003049631 A1	13-03-2003
		US 2003143538 A1	31-07-2003
		US 2002155459 A1	24-10-2002
		US 2003054358 A1	20-03-2003
		US 2002160381 A1	31-10-2002
		US 2002155461 A1	24-10-2002
		US 2002155462 A1	24-10-2002
		US 2003124528 A1	03-07-2003
		US 6812334 B1	02-11-2004
		US 2003068622 A1	10-04-2003
		US 2004219520 A1	04-11-2004
		US 2003148282 A1	07-08-2003
		US 2002182613 A1	05-12-2002
		US 2003044805 A1	06-03-2003
US 2002015215 A1	07-02-2002	NONE	
US 2002055253 A1	09-05-2002	DE 10055421 A1	29-05-2002
		JP 2002192499 A	10-07-2002
WO 0224570 A	28-03-2002	AU 8793601 A	02-04-2002
		GB 2371119 A	17-07-2002
CH 681047 A5	31-12-1992	NONE	
US 5559358 A	24-09-1996	NONE	